

Graphene Nanoribbon Crossbar Nanomesh

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Abstract—Graphene nanoribbon crossbars exhibit negative differential resistance. This nonlinear current-voltage response can be exploited in a nanomesh geometry for high-density logic or memory. A 2xN crossbar array can store 3^N states. Proof-of-principle of a high functional density architecture exploiting the non-linear dynamics of graphene nanoribbon crossbars in a nanomesh geometry is demonstrated.

Keywords- *graphene; crossbar; NDR; NEGF; FIREBALL; multi-valued memory*

I. INTRODUCTION

Lack of a bandgap in two-dimensional (2D) graphene [1-3] reduces its utility for conventional electronic device applications. One way of introducing a bandgap is to stack two monolayers to form a bilayer with a bandgap that can be tuned by creating a potential difference between the two layers [4-7].

Recently, we have shown that independently contacting a top and bottom nanoribbon of two overlapping GNRs results in negative differential resistance (NDR) [8]. A crossbar geometry is most suitable for high density integration of NDR nodes. Simulations of the geometry, shown in Fig. 1, show a current–voltage response plotted in Fig. 2 [9]. This functionality can be exploited in a crossbar GNR (xGNR) network utilizing the nonlinear elements for memory and logic applications.

II. GNR CROSSBAR DEVICE

The model cross-bar GNR (xGNR) structure consists of a vertical and a horizontal semi-infinite, H-passivated, relaxed, armchair type GNR (AGNR) placed one on top of the other at right angles as shown in Fig. 1. The widths of the GNRs are chosen to be 14-C atomic layers ($3n + 2$) ~ 1.8 nm to minimize the bandgap resulting from the finite width. The calculated bandgap of the 14-AGNR is

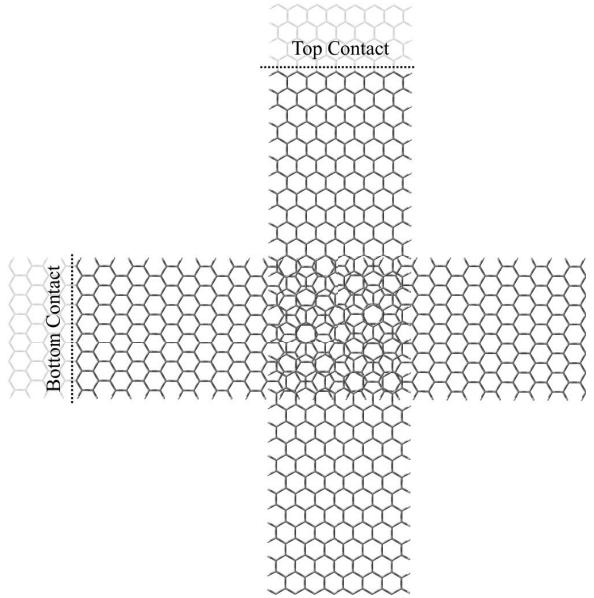


Figure 1: Atomistic geometry of the GNR crossbar. Two hydrogen passivated relaxed armchair GNRs are placed on top of each other at right angles with a vertical separation of 3.35 Å. A bias is applied by independently contacting each GNR.

130 meV which is in good agreement with Son *et al.* [10].

The electronic structures of the GNRs and xGNR are modeled with the quantum molecular dynamics, DFT code, Fireball [11,12], using separable, nonlocal Troullier-Martins pseudopotentials [13], the BLYP exchange correlation functional [14, 15], a self-consistent generalization of the Harris-Foulkes energy functional [16 - 19] and a minimal sp^3 Fireball basis set. The radial cutoffs of the localized pseudoatomic orbitals forming the basis are $r_c^{1s} = 4.10$ Å for hydrogen and $r_c^{2s} = 4.4$ Å and $r_c^{2p} = 4.8$ Å for carbon [20]. Fireball Hamiltonian matrix elements are used in the NEGF algorithm to

calculate the surface self-energies, Green's function of the device, the spectral function, the transmission, and the current as described in [20].

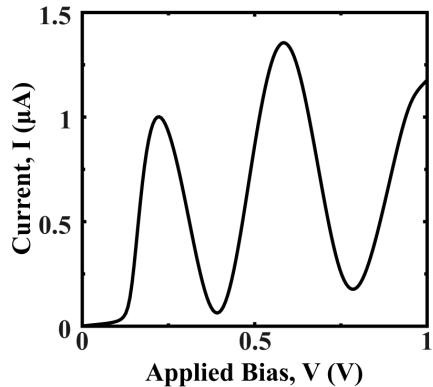


Figure 2: Simulated I - V characteristics of the crossbar structure exhibiting NDR. The first peak and the valley currents occur at 0.2 V and 0.4 V and the second peak and the valley currents occur at 0.6 V and 0.8 V respectively [9].

III. CROSS-BAR FOR DYNAMIC MEMORY & LOGIC

A graphene-based crossbar structure consisting of a number of vertical and horizontal GNRs opens a new horizon for building scalable and ultra-fast memory and logic devices. During the past decade, the crossbar has attracted much attention as a promising approach towards nanometer size architectures exploiting non-linear I - V characteristics at the point of cross junctions [21-25]. In the most cases, the cross-point is considered as a programmable diode. For example, a nanowire crossbar structure with a layer of rotaxane molecules serving as the programmable cross points has been experimentally demonstrated [26]. The NDR of the xGNR structure shown in Fig. 2 can be exploited in a network utilizing the collective dynamics of the nonlinear elements similar to the “neuromorphic” architecture described by V. P. Roychowdhury et al. [27].

A replica of the graphene 2-D crossbar structure comprising vertical GNRs (vGNRs) and horizontal GNRs (hGNRs) and the equivalent circuit are shown in Fig. 3(A) and (B), respectively. There is a 2-D net consisting of horizontal and vertical GNR layers. The vertical and horizontal

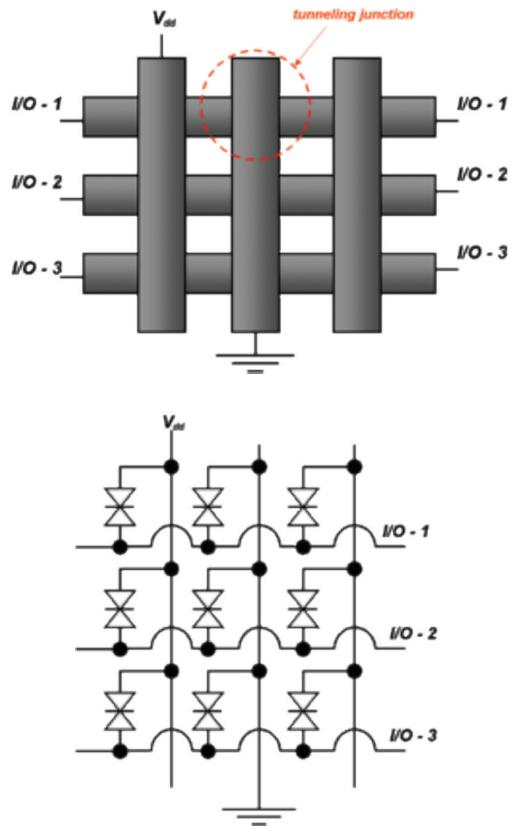


Fig.3 (Top) General view of the GNR cross-bar structure. consisting of vertical and horizontal GNRs. (Bottom) Equivalent circuit consisting of resonant tunneling diodes (RTDs).

layers are assumed to have a linear resistance, while the cross-points are modeled as identical resonant tunneling diodes. In order to illustrate the operation of the network, we carried out numerical simulations based on the piecewise approximation I - V characteristics (Fig.4). Assuming the slopes of the low and high voltage branches the same, the approximate tunneling I - V characteristic is described by the set of 4 parameters: V_p, I_p, V_v, I_v , which are the peak voltage, the peak current, the valley voltage and the valley current, respectively. Thus, for the GNR-junction, we assumed $V_p = 1$, $I_p = 1$, $V_v = 2$, $I_v = 0.01 I_p$. In all graphs all voltages are normalized to $[V_p]$ and all currents are normalized to $[I_p^C]$, respectively. The capacitance of the crosspoint is assumed equal to the quantum capacitance value of $21 \mu\text{F cm}^{-2}$ [28].

The particular choice of tunneling parameters was made arbitrary to simulate the general features of the GNR-based network. The dynamics of the network is well described by the balance equations for each point of junction as follows:

$$\frac{CdV_{i,j}}{dt} = - \left[J_{i,j}^C + J_{i,j}^S + \sum_{\substack{k,l \neq i,j \\ k,l \in R}} J_{ij,kl}^D \right] \quad (1)$$

where $V_{i,j}$ is the potential, C is the capacitance, J^C , J^S and J^D are the vertical, horizontal and the tunneling currents, respectively. Each of these currents is defined by potential difference across the points on the mesh, where the subscripts i and j

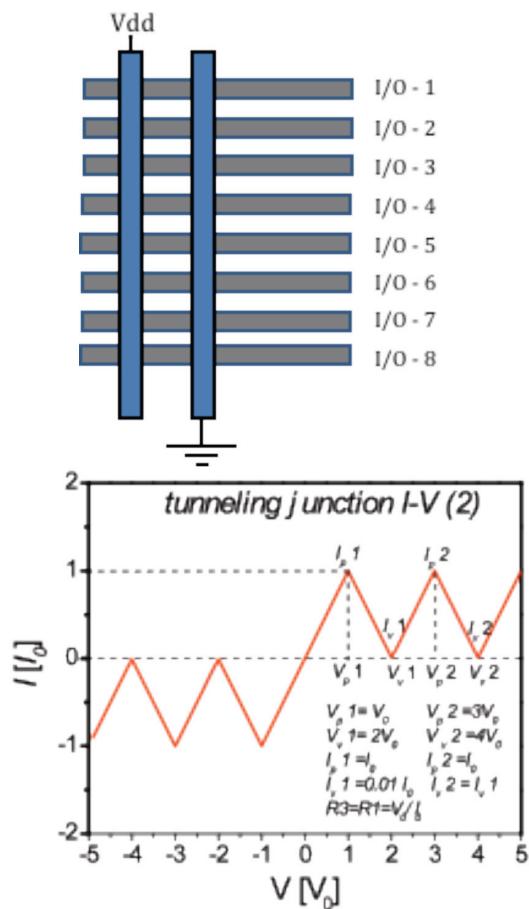


Fig.4 (Top) Schematic of the 2×8 GNR crossbar memory. (Bottom) Piecewise approximation to the xGNR $I-V$ characteristic.

indicates the position in the 2-D lattice. The potentials of the vertical/horizontal edge contacts are considered as the boundary conditions. The model is suitable for logic performance illustration (e.g. output voltages as a function of the boundary conditions, read-in and read-out procedures,

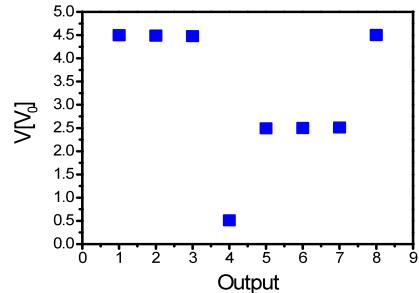


Fig. 5. Results of numerical simulations showing one out of 3^8 possible triple-state outputs ($0.5V$, $2.5V$, and $4.5V$ @ $V_{dd}=5V$).

switching dynamics, etc.).

As an example of the network capabilities for multi-bit dynamic storage, we present the results of numerical modeling by Eq. 1. The voltages are applied to the vGNRs as shown in Fig. 4. In this case, each of the possible paths for the electric current consists of at least two cross-junctions. In turn, the potential of the each hGNR may have three stable states depending on the V_{dd} and the NDR characteristics. The results of numerical simulations in Fig. 5 illustrate one of the 3^8 possible output voltage combinations. The horizontal GNRs can be considered as the input/output ports for information read-in and read-out. The switching between the states on each node can be done independently from others by applying a voltage pulse to the hGNR. In this, case, a cross-bar comprising N hGNRs may store 3^N states.

The model xGNR structure has some promising performance matrices. The area of a single cross point is about $10\text{nm} \times 10\text{nm}$ with a geometrical capacitance of $\sim 0.1 \text{ aF}$. The resistances at the first peak and valley currents are found to be $\sim 200 \text{ k}\Omega$ and $\sim 6 \text{ M}\Omega$ respectively. The typical RC time constant is then in the order of tens of femto

seconds making it suitable for high density terahertz applications. A series combination of two xGNRs, used in above analysis, has three stable points. Assuming $V_{dd} = 1V$, the average power dissipation at these stable points is $\sim 0.5 \mu\text{W}$. The slew rate, defined as the rate at which the output voltage can be changed from one stable point to other, is on the order of 10 THz.

The described graphene nanoribbon crossbar is of great potential for integration with conventional CMOS-based circuitry offering a novel logic unit with almost no area or time delay overhead. The use of the crossbar based on NDR devices has been discussed for a long-time [21-25]. Recent progress in graphene nanoribbon fabrication makes this approach feasible for practical applications. High mobility and nanometer scale size are the main advantages inherent to the graphene. This work presents the results of the theoretical study and numerical modeling aimed to illuminate the potential benefits of using a nanostructure comprising just two graphene nanoribbons.

IV. CONCLUSION.

In conclusion, proof-of-principle of a high functional density architecture exploiting the non-linear dynamics of graphene nanoribbon crossbars in a nanomesh geometry has been demonstrated. The possibility of building a multi-valued memory is illustrated by numerical modeling based on the simulated I-V characteristics of the crossbar structure exhibiting NDR. The GNR crossbar sketched here is an example of plausible architecture to benefit from the unique graphene properties. The described graphene nanoribbon crossbar can be evolved in different ways to make use of the collective dynamics inherent to networks consisting of NDR-base devices. Defect tolerance and integration with CMOS are among the main potential issues which require a more detailed study.

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